

J020 Rec'd PCT/PTO 16 MAY 2005
23294
US Phase of
PCT/JP2004/1214
10/534992LIST OF PATENTS
AND PUBLICATIONS

APPLICANT

Donato ETTORRE et al

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U.S. PATENT DOCUMENTS

EX. INIT		DOCUMENT NO. Entry code - No.	DATE MM-YYYY	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
/LM/	AA	US-5 832 023	11-1998	Latva-aho			
/LM/	BB	US-6 201 828	03-2001	Ei-Tarhuni et al.			
	CC	US-					
	DD	US-					
	EE	US-					
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	GG	US-					
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							YES	NO
	AI							
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	AP							
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

/LM/	AS	"INTERPOLATION IN DIGITAL MODEMS - PART I (IEEE Trans. Comm. March 1993)
/LM/	AT	"INTERPOLATION IN DIGITAL MODEMS - PART II by Erup et al.
/LM/	AU	DIGITAL COMMUNICATIONS by G. Proakis (McGraw-Hill, 1995)

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The Firm of Karl F. Ross P.C.
Customer Number 535
5676 Riverdale Ave., Box 909
Bronx (Riverdale), NY 10471
(718) 884-6600

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	AL						
	AM						
	AN						
	AO						
	AP						
	AR						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

/LM/	AS	"A DIGITAL CHIP TIMING RECOVERY LOOP..." by De Gausenzi (IEEE Trans. 11-1993)
	AT	
	AU	

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Bronx (Riverdale), NY 10471
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